A Student Program

On

"INTRODUCTION TO VLSI"

10th Feb to 13th Dec, 2018

conducted by Mr. M .Rajendra Prasad

Venue: VAAGDEVI COLLEGE OF ENGINEERING (UGC AUTONOMOUS) Permanently Affiliated to JNTUH.

Phone: 0870-2865182, 183, Fax: 2865185

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About the Institution:

Vaagdevi College Engineering, established in 1998 and affiliated to Jawaharlal Nehru Technological University, Hyderabad. It is a premium Institution of Viswambhara Educational Society serving the cause of Technical Education through a large number of academic and technical institutions based in and around the historic city of Warangal, Telangana. The Warangal city is a centre for large number of Educational Institutions including NIT and Kakatiya University. The City of Warangal, an abode of Kakatiya Dynasty, is not only famous for a number of historical monuments but also is a place of tourist attraction. It is well connected to the capital city, Hyderabad and all other important places in the country by road and rail.

The college offers 5 B.Tech and 5 M.Tech. programs in Engineering apart from other post graduate programs in MCA and MBA.

Vaagdevi College of Engineering is established since the inception of the college in 1998 and has qualified and dedicated faculty. The laboratories are well equipped with latest equipment in the field of all departments. Apart from B.Tech. the institution also offers AICTE approved M.Tech. programs in

all departments. Institution is successful in organizing many programs under ISTE & IETE students & faculty chapters.

Objectives:

Providing a platform to enrich the knowledge in the field of Computer Science and Engineering by interacting with the experts from reputed institutions.

About the workshop:

Basic concepts of Anolog Electronic Ciruits

Participants:

Students of 2nd of ECE

Resource person:

Mr. M .Rajendra Prasad

Important Dates:

Last Date of receiving applications:

8th Dec, 2018

Intimation:

9th Dec, 2018

Filled in application forms should be sent to

Vceece64@gmail.com